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SPECIFICATION

The Examiner objected to the following description of Figure 4D (page 12, Line 27-28):
"apply Reed Solomon to recover lost data packets." To overcome this objection, please amend
the sentence in the following manner:

Figure 4D is a diagrammatic representation showing data packet reconstruction. A block buffer 451 includes data packets 431 and 433 with error correction packets 437 and 439. The data packets and the error correction packets are passed to the decoder logic mechanism to allow application of various reconstruction methodologies. According to various embodiments, decoder logic 461 can apply Reed Solomon error correction to recover lost data packets. The error encoding and decoding protocols can be established during tunnel set-up. The decoder logic can be implemented using an application-specific integrated circuit (ASIC), a programmable device such as a field programmable gate array (FPGA), and/or a general purpose processor, etc. In one embodiment, a reconstruct packet is packet 435. The packet 435 is then forwarded to a next network node.